

Digital Delay Units

SERIES: **DDU-4**

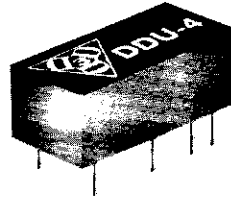
1 to 5 Taps (14 pins DIP)
T²L Interfaced

data
delay
devices, inc.



Features:

- Completely interfaced for TTL and DTL application.
- No external components required.
- P. C. board space economy achieved.
- Low profile.
- Fits standard 14 pins DIP socket.

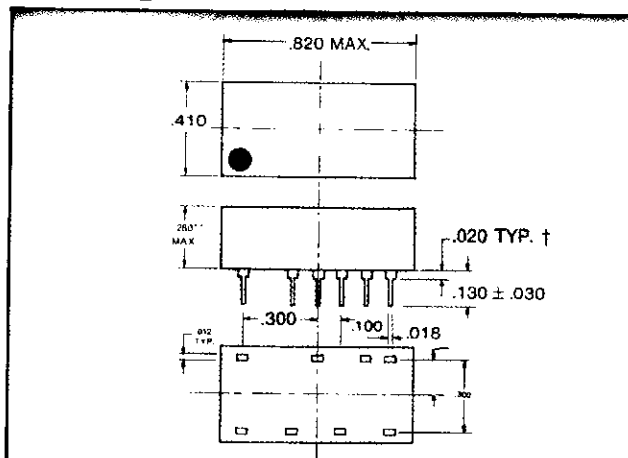


Specifications:

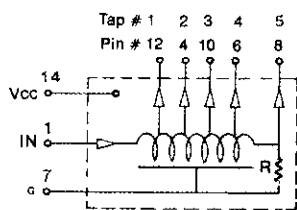
- Delay tolerance: $\pm 5\%$ or better or 2 ns whichever is greater.
- Rise-time: 4 ns typically.
- Minimum pulse width: 20% of total delay.
- Temperature coefficient: 100 PPM/ $^{\circ}\text{C}$.
- Temperature range: -0°C to 70°C standard. (-55°C . to $+125^{\circ}\text{C}$. on request)*
- Supply voltage: 4.5 to 5.5 Vdc.
- Logic 1 input current: 50 μa max.
- Logic 0 input current: -2 ma. max.
- Logic 1 V out: 2.5 V min.
- Logic 0 V out: 0.5 V max.

- Logic 1 Fan-out: 20/tap max.
- Logic 0 Fan-out: 10/tap max.
- Power Dissipation: 375 mw max.

*Add "M" after Part No. Example DDU-4-1010M
**.320 Max. for "M" Units



† or case stand-offs



SCHEMATIC DIAGRAM FOR 5 DELAYS OUTPUT

Single Delay Output (ns)		2 Delays Output (ns)		
Part No.	Delay @ Pin #8	Part No.	Delay @ Pin #10	Delay @ Pin #8
DDU-4-1010	10	DDU-4-2020	10	20
DDU-4-1020	20	DDU-4-2040	20	40
DDU-4-1030	30	DDU-4-2050	25	50
DDU-4-1040	40	DDU-4-2060	30	60
DDU-4-1050	50	DDU-4-2080	40	80
DDU-4-1060	60	DDU-4-2100	50	100
DDU-4-1080	80	DDU-4-2150	75	150
DDU-4-1100	100	DDU-4-2200	100	200
DDU-4-1150	150	DDU-4-2300	150	300
DDU-4-1200	200	DDU-4-2400	200	400
DDU-4-1300	300	DDU-4-2500	250	500
DDU-4-1400	400	DDU-4-7690A	65	110
DDU-4-1500	500			

5 Delays Output (ns)					
Part No.	Delay @ Pin #12	Delay @ Pin #4	Delay @ Pin #10	Delay @ Pin #6	Delay @ Pin #8
DDU-4-5025	5	10	15	20	25
DDU-4-5050	10	20	30	40	50
DDU-4-5075	15	30	45	60	75
DDU-4-5100	20	40	60	80	100
DDU-4-5125	25	50	75	100	125
DDU-4-5150	30	60	90	120	150
DDU-4-5200	40	80	120	160	200
DDU-4-5250	50	100	150	200	250
DDU-4-5300	60	120	180	240	300
DDU-4-5400	80	160	240	320	400
DDU-4-5500	100	200	300	400	500